

PATENTS

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Douglas D. Coolbaugh, et al.

Examiner: Cuong Q. Nguyen

Serial No: 09/516,615

Art Unit: 2811

Filed: March 1, 2000

Docket: BUR919990190US1 (13020)

For: METHOD OF FABRICATING A  
POLYSILICON CAPACITOR UTILIZING  
FET AND BIPOLAR BASE POLYSILICON LAYERS

Dated: March 4, 2002

Assistant Commissioner for Patents  
United States Patent and Trademark Office  
Washington, D.C. 20231

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AMENDMENT AND RESPONSE

Sir:

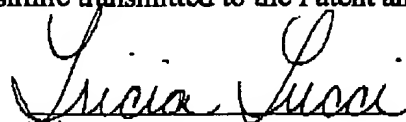
In response to the Office Action dated December 5, 2001, applicants submit the following amendments and remarks for entry of record in the above-identified patent application.

IN THE SPECIFICATION:

Please amend the paragraph on Page 6, lines 10-17 to read as follows:

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that this paper is being facsimile transmitted to the Patent and Trademark Office on the date shown below.

Dated: March 4, 2002  
Tricia Tucci